

Fig.1

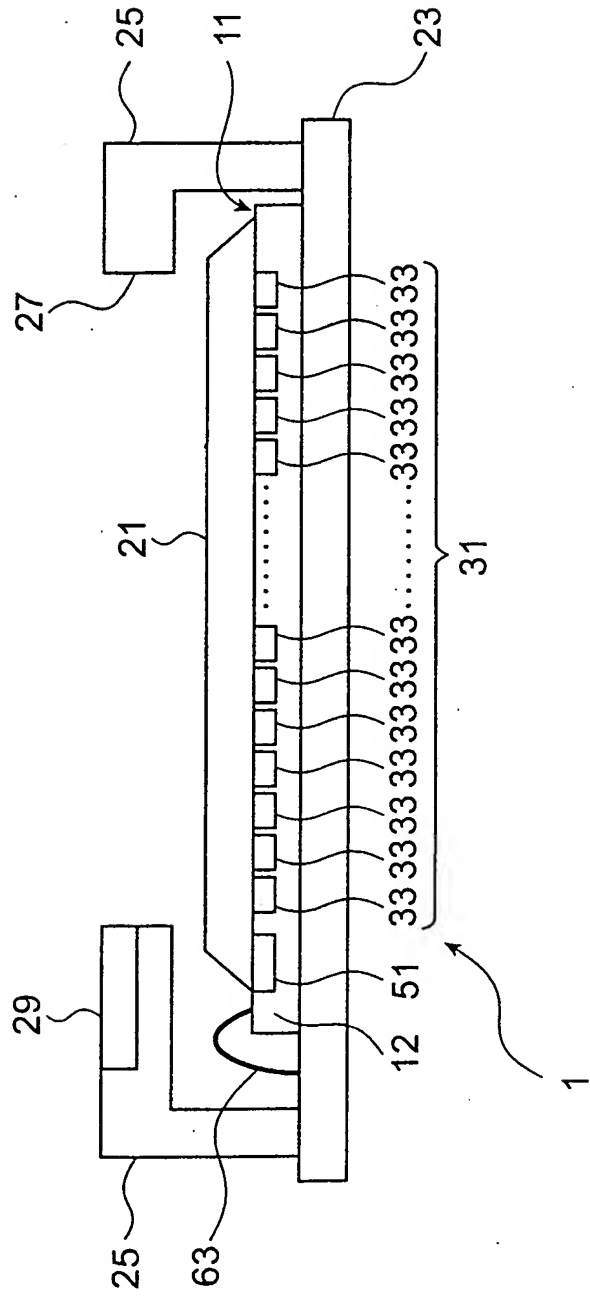


Fig. 2

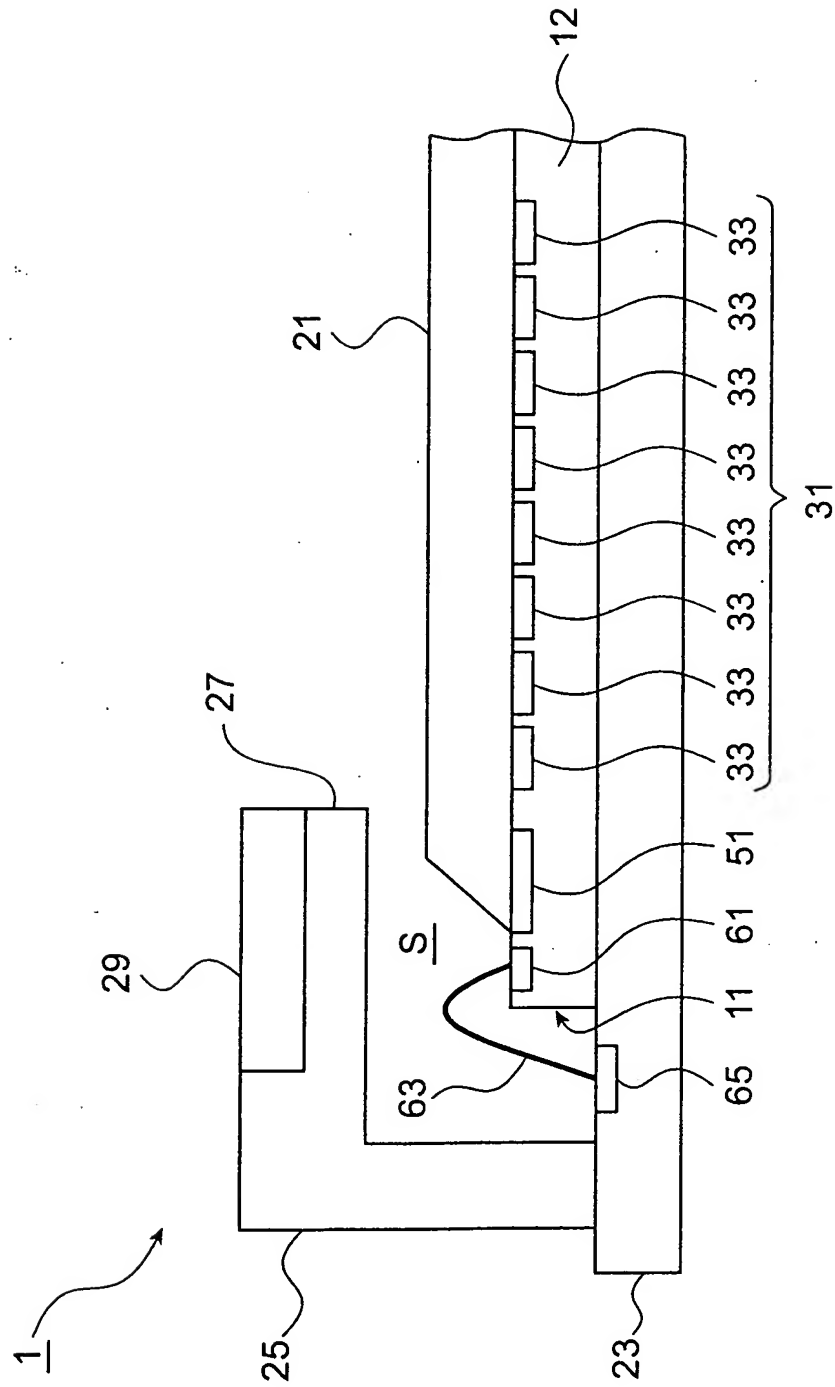
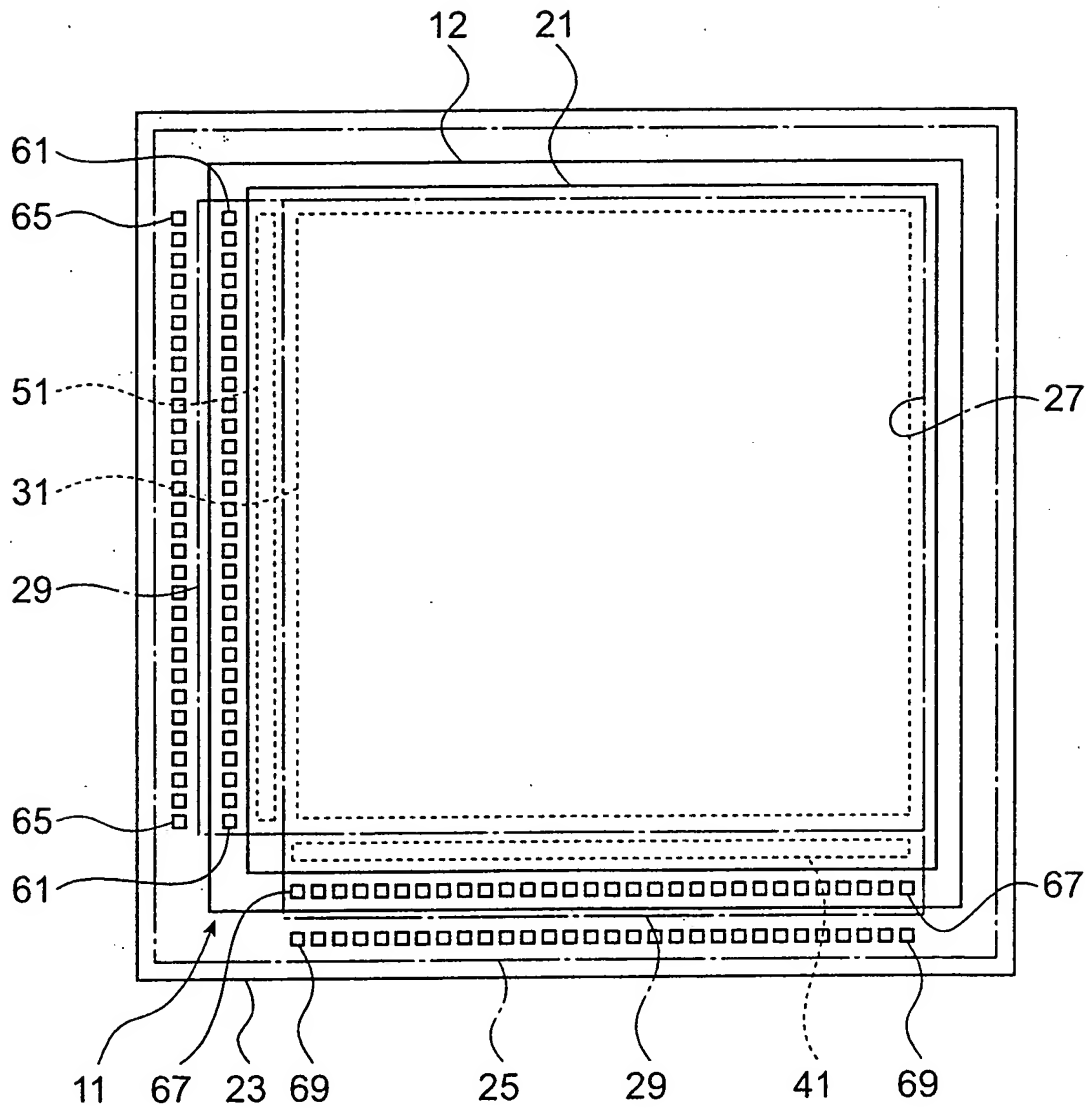


Fig.3



The diagram illustrates a semiconductor device with a grid of pixels. Each pixel (33) contains a photodiode (35) and is connected to a vertical shift register (41) via a vertical gate (43). The vertical shift register is connected to a horizontal shift register (59) via a horizontal gate (55). The horizontal shift register is connected to an output (60) via a gate (57). The device is controlled by a series of gates (31, 45, 53, 55, 57) and a coordinate system (x, y) is shown at the bottom right.

Fig.5

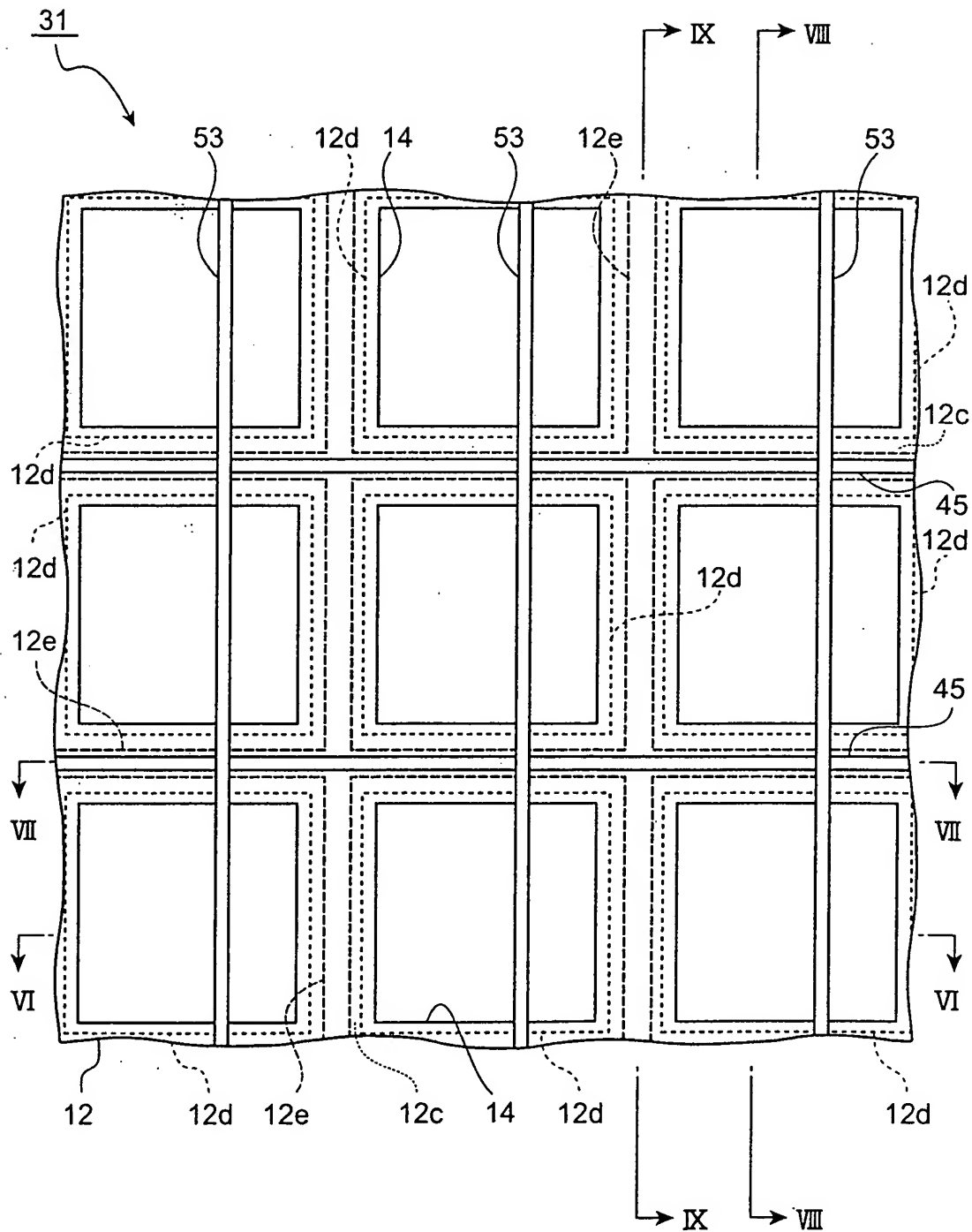


Fig.6

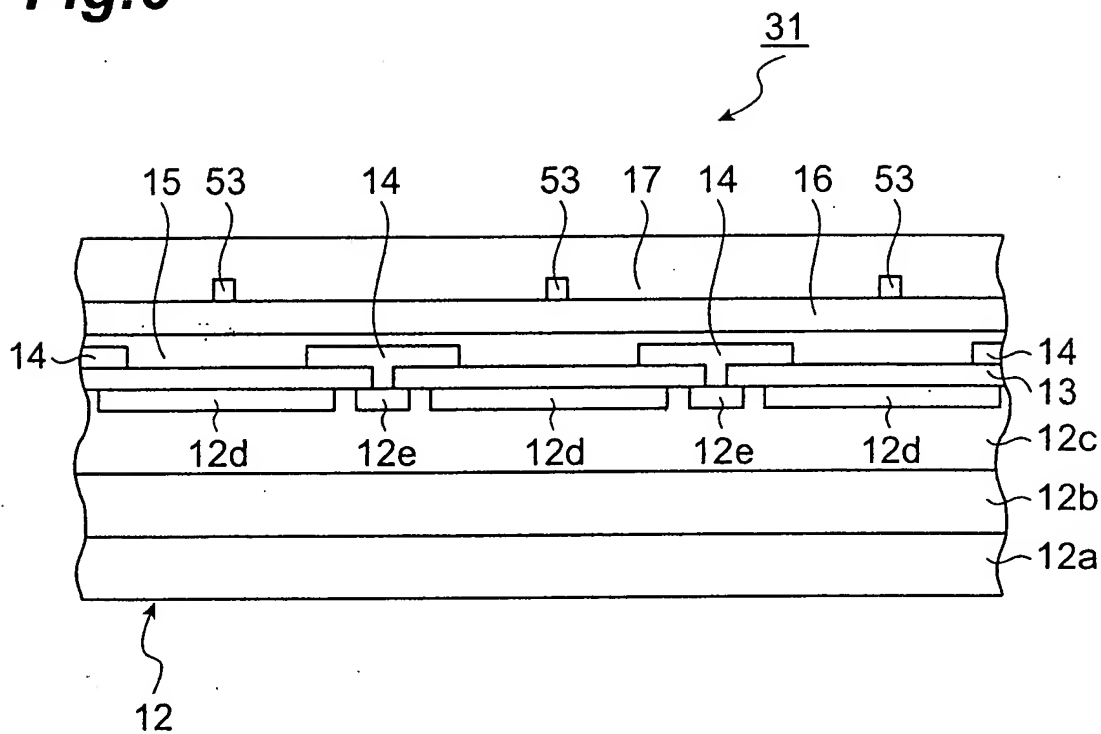
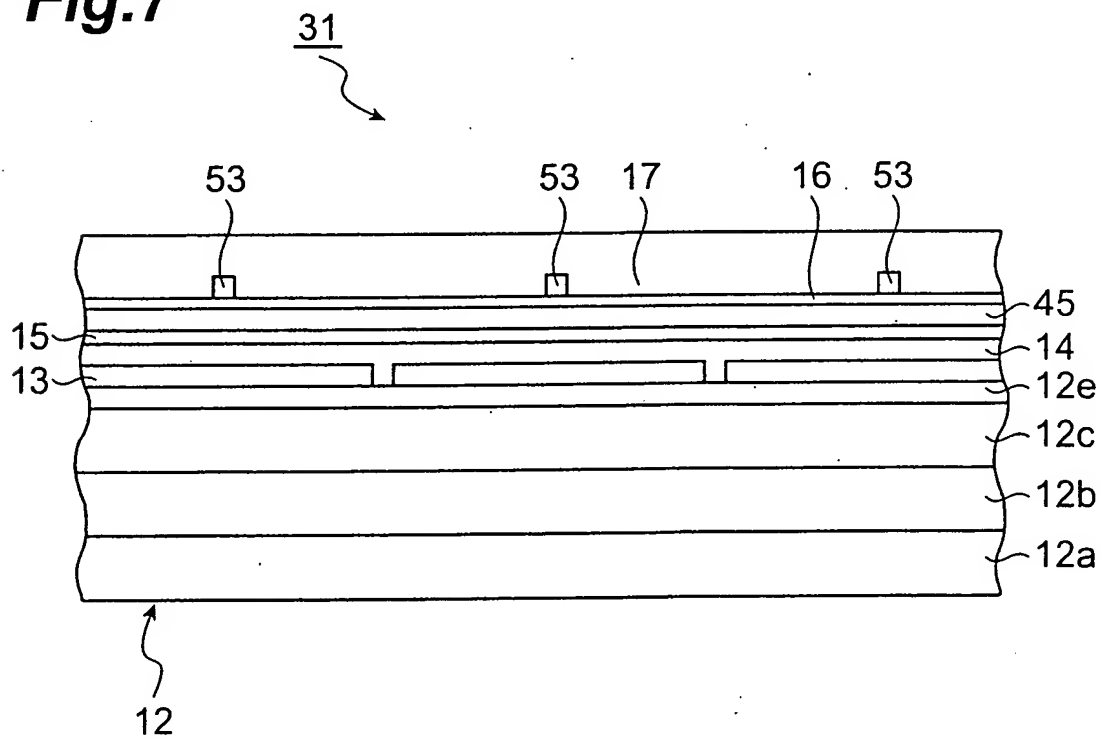


Fig.7



31



31



Fig.10

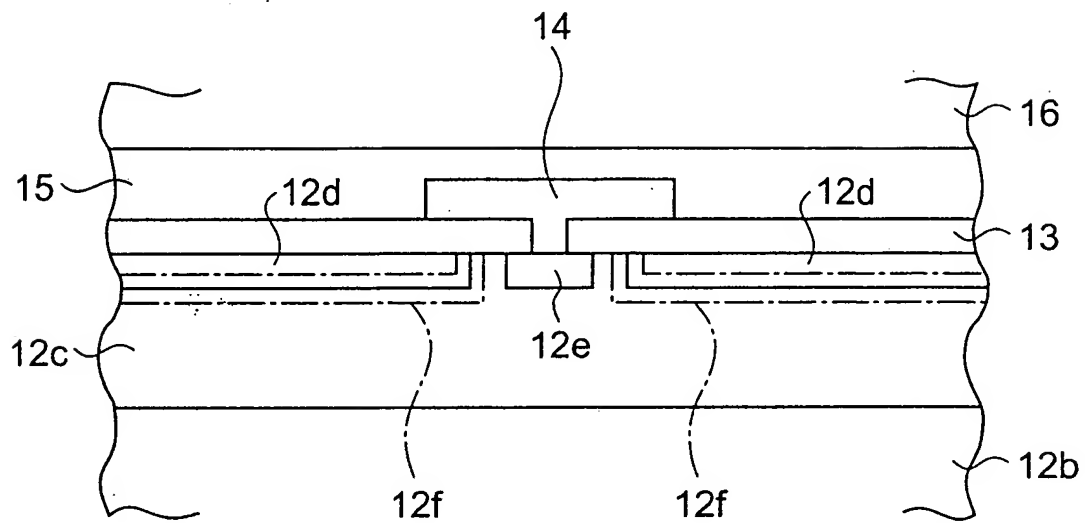


Fig.11

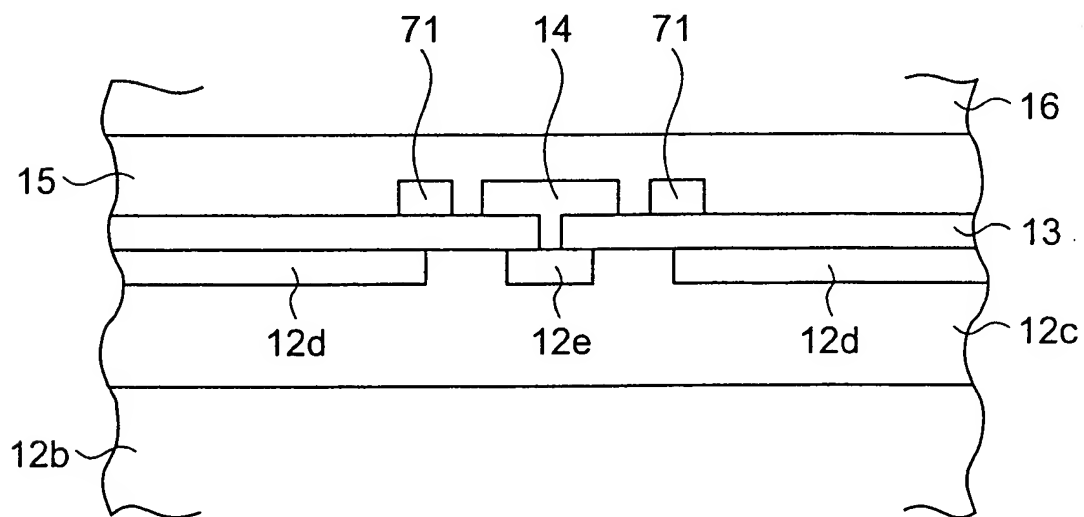


Fig.12

